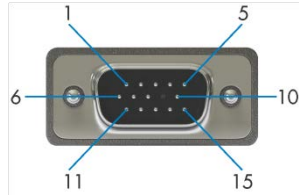
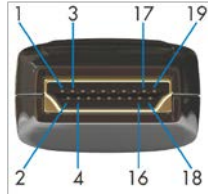


HD15 VGA Connector



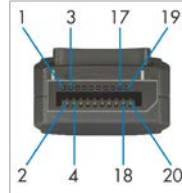
| Pin Number | Pin Function | Explanation of the Function |
|------------|-------------------|---|
| 1 | Red Video | Carries the Red color information of the video signal |
| 2 | Green Video | Carries the Green color information of the video signal |
| 3 | Blue Video | Carries Blue color information of the video signal |
| 4 | Reserved | Before E-DDC this carried identification information from the monitor, this pin is now Reserved/Unused |
| 5 | H.Sync Ground | Electrical ground for the Horizontal Sync signal |
| 6 | Red Ground | Electrical ground for the red color information of the video signal |
| 7 | Green Ground | Electrical ground for the green color information of the video signal |
| 8 | Blue Ground | Electrical ground for the blue color information of the video signal |
| 9 | Reserved/+5VDC | Reserved/Unused. In the original specification this carried power from the video card to a monitor for the purpose of supporting future features. |
| 10 | V.Sync/DDC Ground | Electrical ground for the Vertical Sync and DDC signals |
| 11 | Reserved | Before E-DDC this carried identification information from the monitor, this pin is now Reserved/Unused |
| 12 | DDC Data (SDA) | Carries identification information, EDID, from the monitor. |
| 13 | H.Sync | Carries Horizontal Sync signal used to build the video image |
| 14 | V.Sync | Carries Vertical Sync signal used to build the video image |
| 15 | DDC Clock (SCL) | Carries identification information, EDID, from the monitor. |

HDMI Type A Connector



| Pin Number | Pin Function | Explanation of the Function |
|------------|--------------------|---|
| 1 | TMDS Data 2 + | Carries audio and video data |
| 2 | TMDS Data 2 Shield | Electrical ground for the audio and video TMDS lines |
| 3 | TMDS Data 2 - | Carries audio and video data |
| 4 | TMDS Data 1 + | Carries audio and video data |
| 5 | TMDS Data Shield | Electrical ground for the audio and video TMDS lines |
| 6 | TMDS Data 1 - | Carries audio and video data |
| 7 | TMDS Data 0 + | Carries audio and video data |
| 8 | TMDS Data 0 Shield | Electrical ground for the audio and video TMDS lines |
| 9 | TMDS Data 0 - | Carries audio and video data |
| 10 | TMDS Clock + | Carries information that aids in signal synchronization |
| 11 | TMDS Clock Shield | Electrical ground for the TMDS Clock lines |
| 12 | TMDS Clock - | Carries information that aids in signal synchronization |
| 13 | CEC | Carries control and command data between devices, Consumer Electronics Control (CEC). |
| 14 | Reserved | Unused. Reserved for future use |
| 15 | DDC Clock (SCL) | Carries identification information, EDID, between the source and display device |
| 16 | DDC Data (SDA) | Carries identification information, EDID, between the source and display device |
| 17 | DDC/CEC Ground | Electrical ground for the DDC and CEC signals |
| 18 | +5V Power | Carries a +5VDC power supply from the source device |
| 19 | Hot Plug Detect | Monitors power up/down and connect/disconnect events |

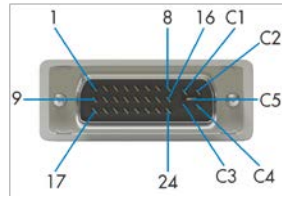
DisplayPort External Connector



| Pin Number | Pin Function | Explanation of the Function |
|------------|-------------------------|---|
| 1 | Main Link Lane 0 + | Carries audio and video data |
| 2 | Main Link Lane 0 Ground | Electrical ground for the audio and video link lines |
| 3 | Main Link Lane 0 - | Carries audio and video data |
| 4 | Main Link Lane 1 + | Carries audio and video data |
| 5 | Main Link Lane 1 Ground | Electrical ground for the audio and video link lines |
| 6 | Main Link Lane 1 - | Carries audio and video data |
| 7 | Main Link Lane 2 + | Carries audio and video data |
| 8 | Main Link Lane 2 Ground | Electrical ground for the audio and video link lines |
| 9 | Main Link Lane 2 - | Carries audio and video data |
| 10 | Main Link Lane 3 + | Carries audio and video data |
| 11 | Main Link Lane 3 Ground | Electrical ground for the audio and video link lines |
| 12 | Main Link Lane 3 - | Carries audio and video data |
| 13 | Ground | Electrical ground |
| 14 | Ground | Electrical ground |
| 15 | Aux Ch + | Bidirectional data carried between the monitor and the source |
| 16 | Aux Ch Ground | Electrical ground for the Aux channel |
| 17 | Aux Ch - | Bidirectional data carried between the monitor and the source |
| 18 | Hot Plug Detect | Monitors power up/down and connect/disconnect events |
| 19 | Power Return | Power return from the monitor |
| 20 | Power (3.3V 500mA) | Power provided to the monitor |

This is the pinout for the source-side connector; the sink-side connector pinout will have lanes 0-3 reversed in order; i.e., lane 3 will be on pin 1 and 3 while lane 0 will be on pin 10 and 12.

DVI Connector



| Pin | Function | Description |
|-----|----------------------------|---|
| 1 | TMDS Data 2 - | Carries Video Data |
| 2 | TMDS Data 2 + | Carries Video Data |
| 3 | TMDS Data 2/4 Shield | Electrical ground for the video TMDS lines |
| 4 | TMDS Data 4 - | Carries Video Data (Dual-Link only) |
| 5 | TMDS Data 4 + | Carries Video Data (Dual-Link only) |
| 6 | DDC Clock (SCL) | Carries identification information, EDID, from the monitor. |
| 7 | DDC Data (SDA) | Carries identification information, EDID, from the monitor. |
| 8 | V.Sync (DVI-I only) | Carries Vertical Sync information used to build an analog video image |
| 9 | TMDS Data 1 - | Carries Video Data |
| 10 | TMDS Data 1 + | Carries Video Data |
| 11 | TMDS Data 1/3 Shield | Electrical ground for the video TMDS lines |
| 12 | TMDS Data 3 - | Carries Video Data (Dual-Link only) |
| 13 | TMDS Data 3 + | Carries Video Data (Dual-Link only) |
| 14 | +5V Power | Carries a +5VDC power supply from the source device |
| 15 | Ground | Electrical ground |
| 16 | Hot Plug Detect | Monitors power up/down and connect/disconnect events |
| 17 | TMDS Data 0 - | Carries Video Data |
| 18 | TMDS Data 0 + | Carries Video Data |
| 19 | TMDS Data 0/5 Shield | Electrical ground for the Video TMDS lines |
| 20 | TMDS Data 5 - | Carries Video Data (Dual-Link only) |
| 21 | TMDS Data 5 + | Carries Video Data (Dual-Link only) |
| 22 | TMDS Clock Shield | Electrical ground for the TMDS Clock lines |
| 23 | TMDS Clock + | Carries information that aids in signal synchronization |
| 24 | TMDS Clock - | Carries information that aids in signal synchronization |
| C1 | Red Video (DVI-I only) | Carries the Red video information of an analog video signal. DVI-I only |
| C2 | Green Video (DVI-I only) | Carries the Green video information of an analog video signal. DVI-I only |
| C3 | Blue Video (DVI-I only) | Carries Blue video information of an analog video signal. DVI-I only |
| C4 | H.Sync (DVI-I only) | Carries Horizontal Sync signal used to build an analog video image |
| C5 | Common Ground (DVI-I only) | Electrical ground for Red, Green, and Blue video data. Unused in a DVI-D cable |